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Banpil Announces Interconnect Simulation Tool S-Factor Software

Banpil S-Factor enables high-speed and low-power designs, available for free trial

Santa Clara, California – December 15, 2010 - Banpil Photonics, Inc., a leading company in expanding the boundaries of optics and electronics through innovations, today announced the beta availability of its high-speed interconnect design simulation tool S-Factor software. The Banpil S-Factor simulation tool is a first of its kind library that enables system designers to design and simulate chip-to-chip, board-to-board, and rack-to-rack interconnects incorporating Banpil's patented metallic-interconnects capable of up to 40-Gb/s data transmission using conventional board materials (e.g. FR4). S-Factor will help engineers to evaluate system-level performance when connecting Banpil's interconnects with other components and help to increase performance by more than 6 times while significantly reducing power consumption by as much as 90% compared to conventional solutions.

S-Factor can be requested free of charge from Banpil's website at www.banpil.com/designtools.htm. It is packaged on a free standing software CD as a chip-to-chip interconnect simulation tool making it easy for users to install and use. S-Factor currently includes models of Banpil's interconnects on FR4-PCB for chip-to-chip, board-to-board, rack-to-rack, and soon flexible-printed-circuit (FPC) solutions. With the release of S-Factor, users are able to generate and save S-Parameters with Banpil tools without the need for third-party EDA software.

S-Factor adds to a rich set of simulation technologies by enabling designers to fully characterize and optimize 20-Gb/s and above designs for next generation applications in high-speed systems. System designers now can incorporate Banpil's breakthrough high-speed interconnects and other components into their integrated design environment for system and circuit simulation, along with S-Parameters, schematic capture, layout, and verification capability.

"We are very pleased to release the Banpil S-Factor simulation tool and helping engineers to design next generation high-speed systems," said Dr. Achyut Dutta, Banpil's CEO. "S-Factor is an important step for us because we are now able to provide system designers with first-hand opportunity to exploit, analyze, and verify tremendous performance enhancements that our high-speed interconnects are capable of providing. We previously announced breakthroughs in our metallic-interconnects requiring significantly less power to drive high-speed signals. We showed that Banpil interconnects can make >10-Gb/s signals in over 1.5-meter long rigid-FR4 printed-circuit-board (PCB) and >20-Gb/s in over 2-meters long FPC a reality that architects now can design, simulate, and implement into their practical systems applications, In addition, design-simulation results obtained using BISL are well correlated with experimental results."

S-Factor requires users to have a Windows PC environment to install and operate. After designing and running simulations of Banpil high-speed interconnects, users need to contact Banpil at http://www.banpil.com/contact_us.htm regarding production implementation and licensing options. Banpil also welcomes opportunities to work with system vendors on new applications, and is actively seeking licensees, strategic partnerships with PCB and FPC manufacturers, and investors.

About Banpil Photonics, Inc.

Banpil Photonics develops and licenses fundamental technology expanding the boundaries of optics and electronics. The company has developed an extensive IP portfolio of high-speed interconnects, multispectral image-sensors, and high-efficiency photovoltaic technologies. Banpil innovations enable the development and manufacture of new generations of low-cost, high-speed electrical interconnects for chip-to-chip, board-to-board, and rack-to-rack applications; multispectral image-sensors for automotive and medical imaging, remote-sensing, and communication applications; and photovoltaic technology for solar cell applications. For more information, visit www.banpil.com.

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